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*Has received an application for a patent for a new and useful invention. The title and description of the invention are enclosed. The requirements of law have been complied with, and it has been determined that a patent on the invention shall be granted under the law.*

*Therefore, this*

United States Patent

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*If this application was filed prior to June 8, 1995, the term of this patent is the longer of seventeen years from the date of grant of this patent or twenty years from the earliest effective U.S. filing date of the application, subject to any statutory extension.*

*If this application was filed on or after June 8, 1995, the term of this patent is twenty years from the U.S. filing date, subject to any statutory extension. If the application contains a specific reference to an earlier filed application or applications under 35 U.S.C. 120, 121 or 365(c), the term of the patent is twenty years from the date on which the earliest application was filed, subject to any statutory extension.*

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Attest



US005798746A

**United States Patent** [19]

Koyama

[11] **Patent Number:** 5,798,746[45] **Date of Patent:** Aug. 25, 1998[54] **LIQUID CRYSTAL DISPLAY DEVICE**[75] **Inventor:** Jun Koyama, Kanagawa, Japan[73] **Assignee:** Semiconductor Energy Laboratory Co., Ltd., Kanagawa, Japan[21] **Appl. No.:** 362,881[22] **Filed:** Dec. 23, 1994[30] **Foreign Application Priority Data**Dec. 27, 1993 [JP] Japan ..... 5-354091  
Dec. 27, 1993 [JP] Japan ..... 5-354092[51] **Int. Cl.<sup>6</sup>** ..... G09G 3/36; G02F 1/13[52] **U.S. Cl.** ..... 345/98; 345/92; 345/89[58] **Field of Search** ..... 345/89, 100, 92,  
345/98, 90; 359/79, 56, 87[56] **References Cited****U.S. PATENT DOCUMENTS**4,773,738 9/1988 Hayakawa et al. .... 359/56  
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5,642,129 6/1997 Zavracky et al. .... 345/92*Primary Examiner*—Mark R. Powell*Assistant Examiner*—John Suraci*Attorney, Agent, or Firm*—Sixbey, Friedman, Leedom & Ferguson, P.C.; Gerald J. Ferguson, Jr.[57] **ABSTRACT**

In an active matrix type liquid crystal display device, a time gradation display manner for performing time gradation display is used. A digital gradation (voltage) signal on a signal line is supplied to the digital memory circuit arranged in vicinity of each pixel electrode and stored therein for a desired period of time. This storage state is held until next scanning is started. A high voltage or a low voltage supplied as a power source voltage of the digital memory circuit is applied to a pixel electrode while the digital memory circuit is in a storage state, so that a desired voltage is stably applied to the pixel electrode.

**29 Claims, 8 Drawing Sheets**